



# Exercise 1: Thévenin Equivalent & RC-Circuits

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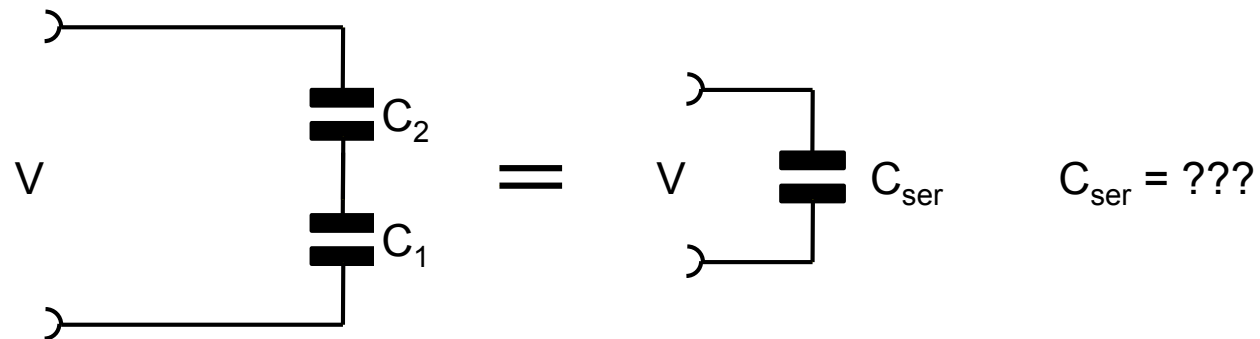
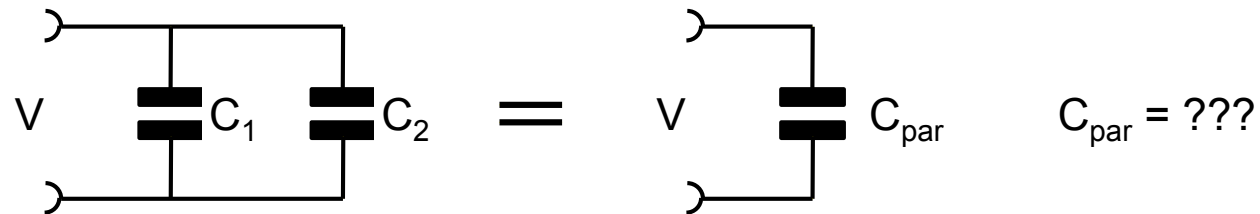
# Recommendations

- I strongly recommend to use a mathematical program (Mathematica, Maple, SageMath,..) to solve the exercises
  
- Inspect each result:
  - What happens for  $\omega \rightarrow 0, \infty$  ?
  - What happens if component values go to 0 or  $\infty$ ?



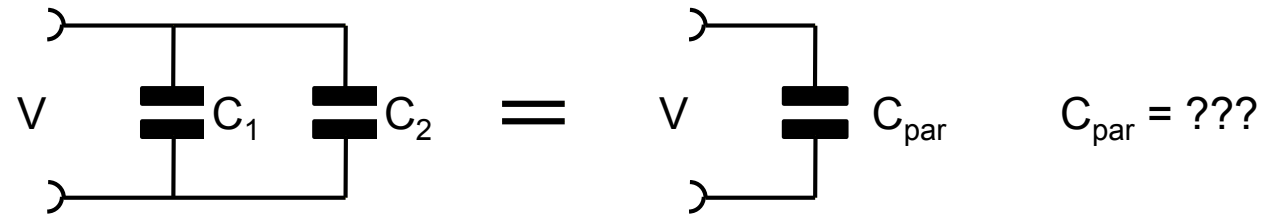
# Exercise 1.0

- Derive the expressions for the series and parallel connection of capacitors using
  - Charge conservation
  - Complex impedance & Kirchhoff's law





# Solution 1.0



## 1. Charge conservation:

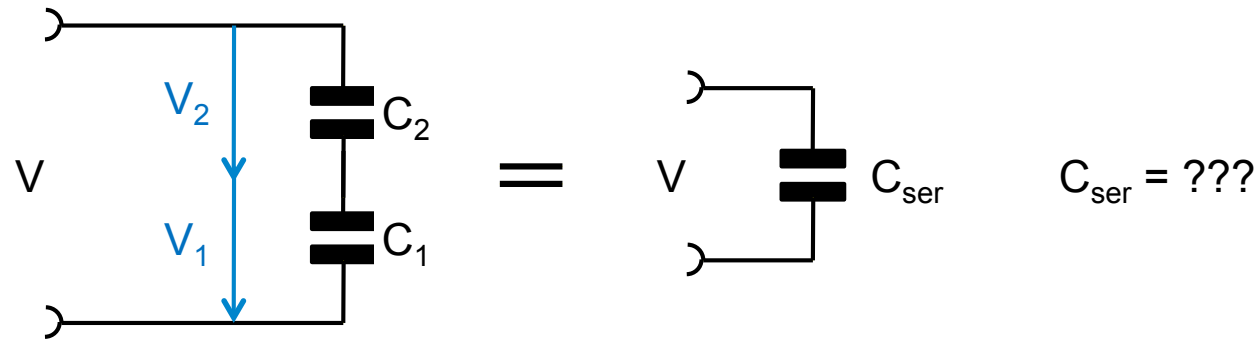
$$V \times C_1 + V \times C_2 = Q_1 + Q_2 = Q_{\text{par}} = V \times C_{\text{par}} \rightarrow C_1 + C_2 = C_{\text{par}}$$

## 2. Kirchhoff & complex impedance:

$$V sC_1 + V sC_2 = i_1 + i_2 = i_{\text{par}} = V sC_{\text{par}} \rightarrow C_1 + C_2 = C_{\text{par}}$$



# Solution 1.0



## 1. Charge conservation:

Note: no charge can 'escape' the middle node, so that  $Q_1 = Q_2 = Q_{\text{ser}}$

$$V = V_1 + V_2 = Q_1/C_1 + Q_2/C_2 = Q/C_1 + Q/C_2 = Q/C_{\text{ser}}$$

$$\rightarrow 1/C_1 + 1/C_2 = 1/C_{\text{ser}}$$

## 2. Kirchhoff & complex impedance:

$$V_1 sC_1 = V_2 sC_2 \quad \text{and} \quad V_1 + V_2 = V \quad \rightarrow \quad V_1 = V C_2 / (C_1 + C_2)$$

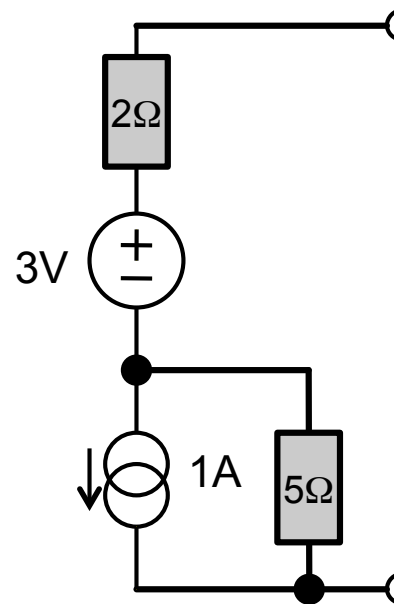
$$\rightarrow i_1 = V_1 sC_1 = V s C_1 C_2 / (C_1 + C_2)$$

$$\rightarrow C_{\text{ser}} = i / (Vs) = i_1 / (Vs) = C_1 C_2 / (C_1 + C_2)$$



## Exercise 1.1

- Derive the Thévenin Equivalent for the following circuit:



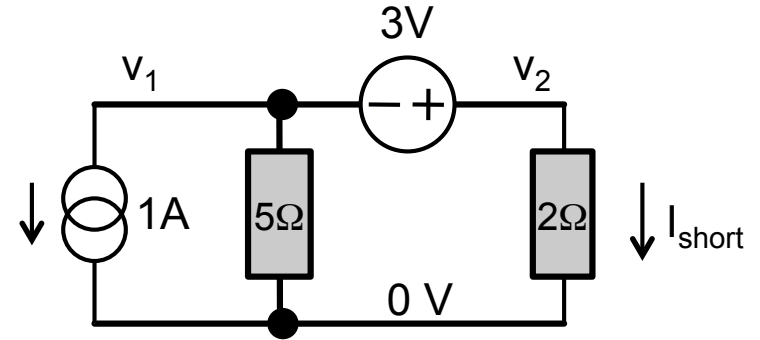
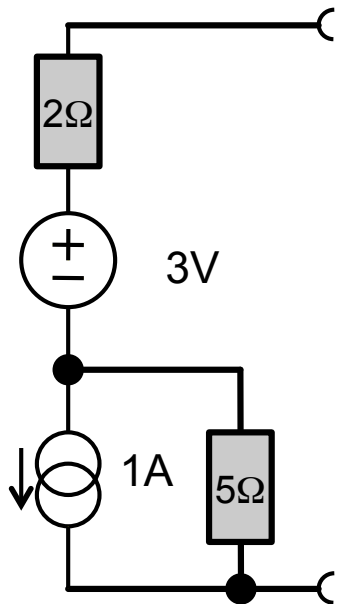
- Try two different methods:
  - Use the Open/Short method with Kirchhoff's rules
  - Convert the I-source part to a voltage source first...



# Solution 1.1 – Kirchhoff

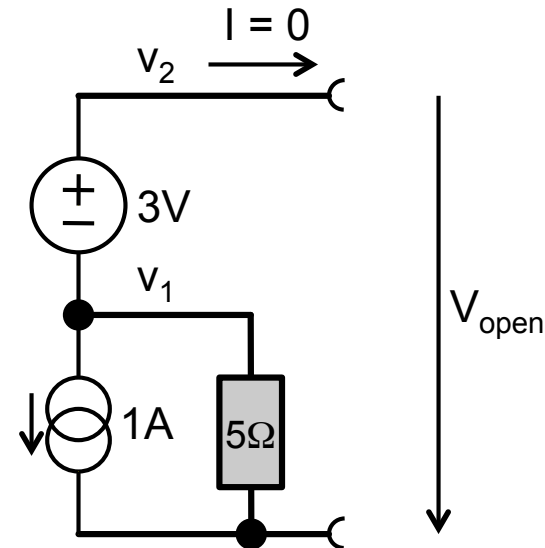
## 1. Short circuit current:

- EQ1:  $1 \text{ A} + v_1 / 5\Omega + v_2 / 2\Omega = 0$
- EQ2:  $v_2 = v_1 + 3\text{V}$
- $\rightarrow v_2 = -4 / 7 \text{ V}$
- $\rightarrow I_{\text{short}} = -2 / 7 \text{ A}$



## 2. Open circuit voltage:

- EQ1:  $1 \text{ A} + v_1 / 5\Omega = 0$
- EQ2:  $v_2 = v_1 + 3\text{V}$
- $\rightarrow v_1 = -5 \text{ V}$
- $\rightarrow v_2 = V_{\text{open}} = -2 \text{ V}$

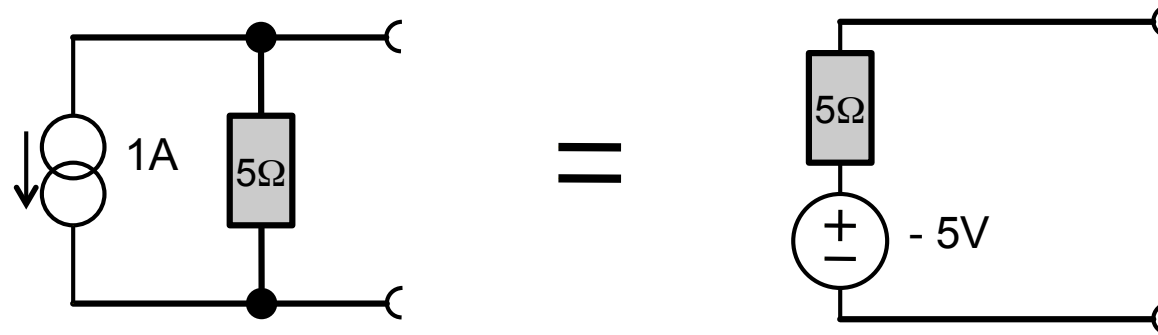


- Source:  $V_0 = V_{\text{open}} = -2 \text{ V}$ ,  $R_V = V_0 / I_{\text{short}} = 7 \Omega$

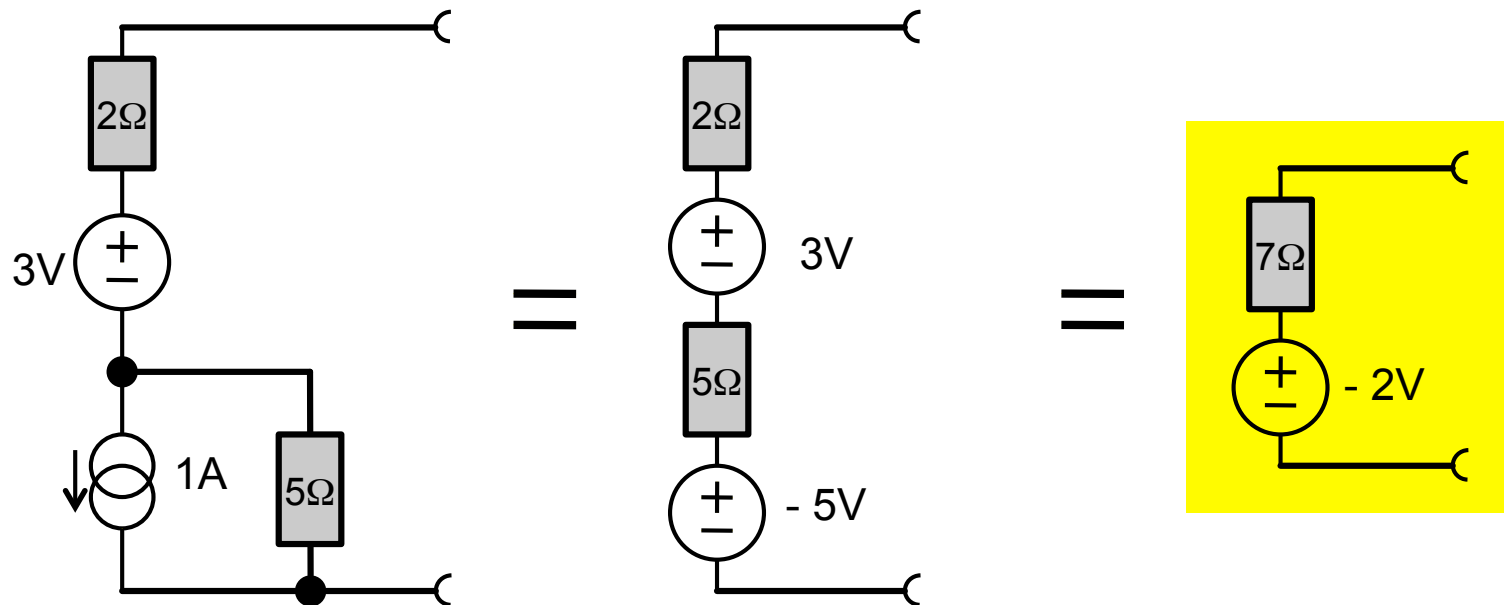


# Solution 1.1 – Thévenin Transformations

1. Convert the current source to a voltage source:



2. Use this in the circuit:

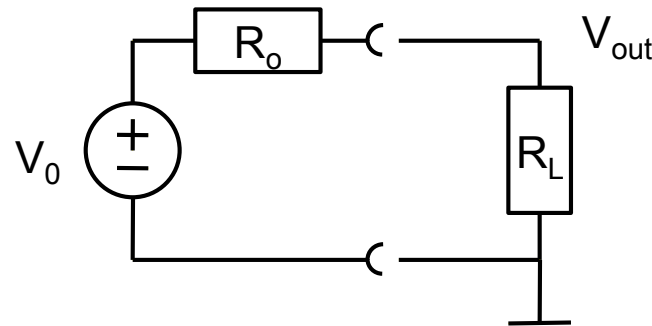






## Exercise 1.2

- A voltage source with voltage  $V_0$  and output resistance  $R_0$  is loaded by a resistor  $R_L$ :



- What is the output voltage  $V_{out}$  ?
- Which current flows in  $R_L$  ?
- What power is dissipated in  $R_L$  ?
  - Check that nothing is dissipated for  $R_L=0$  and  $R_L \rightarrow \infty$
- For which value of  $R_L$  is the dissipation maximized?
  - What is the dissipation?



# Solution 1.2

```
In[29]:= Vout =  $V_0 \frac{R_L}{R_0 + R_L}$  ;
```

```
In[30]:= Iout =  $\frac{Vout}{R_L}$ 
```

```
Out[30]=  $\frac{V_0}{R_0 + R_L}$ 
```

```
In[31]:= Pout = Vout Iout
```

```
Out[31]=  $\frac{R_L V_0^2}{(R_0 + R_L)^2}$ 
```

```
In[38]:= Table[Limit[Pout, RL → x], {x, {0, ∞}}
```

```
Out[38]= {0, 0}
```

```
In[39]:= Solve[D[Pout, RL] == 0, RL] // First
```

```
Out[39]= {RL → R0}
```

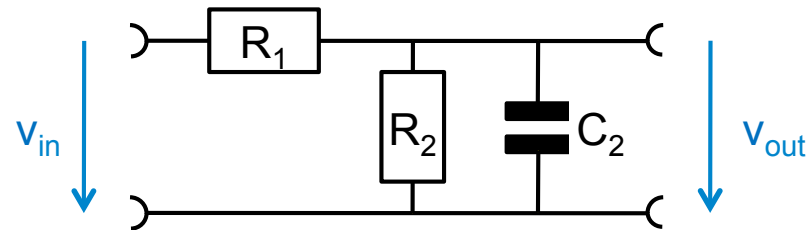
```
In[40]:= Pout /. %
```

```
Out[40]=  $\frac{V_0^2}{4 R_0}$ 
```



## Exercise 1.3

- Derive the Transfer Function of this circuit:



- Use 3 different approaches:
  - Treat the circuit directly (using Kirchhoff's rule)
  - Consider it as a voltage divider of two Impedances. Use  $R_1$  for  $Z_1$  and the parallel connection of  $R_2$  and  $C_2$  for  $Z_2$
  - Replace the (resistive) voltage divider by its Thévenin equivalent and then add the capacitor
- Make a Bode Plot
  - Describe the difference to the normal Low Pass Filter



# Solution 1.3

## Direct Treatment:

$$\text{EQ} = \frac{V_{in} - V_{out}}{R_1} == V_{out} s C_2 + \frac{V_{out}}{R_2};$$

`Solve[EQ, Vout] // First`

$$\left\{ V_{out} \rightarrow \frac{R_2 V_{in}}{R_1 + R_2 + C_2 R_1 R_2 s} \right\}$$

$$\text{Hdirect} = \frac{V_{out}}{V_{in}} /. \%$$

$$\frac{R_2}{R_1 + R_2 + C_2 R_1 R_2 s}$$

## Voltage Divider:

$$\text{Hdiv} = \frac{Z_2}{Z_1 + Z_2} /. \left\{ Z_1 \rightarrow R_1, Z_2 \rightarrow \left( \frac{1}{R_2} + s C_2 \right)^{-1} \right\} // \text{Simplify}$$

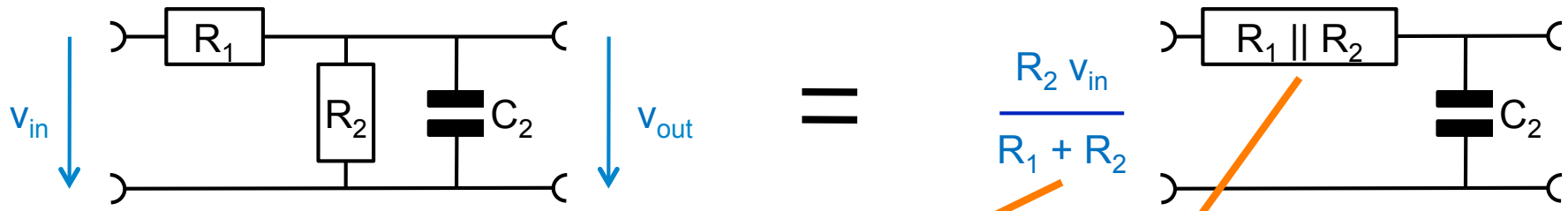
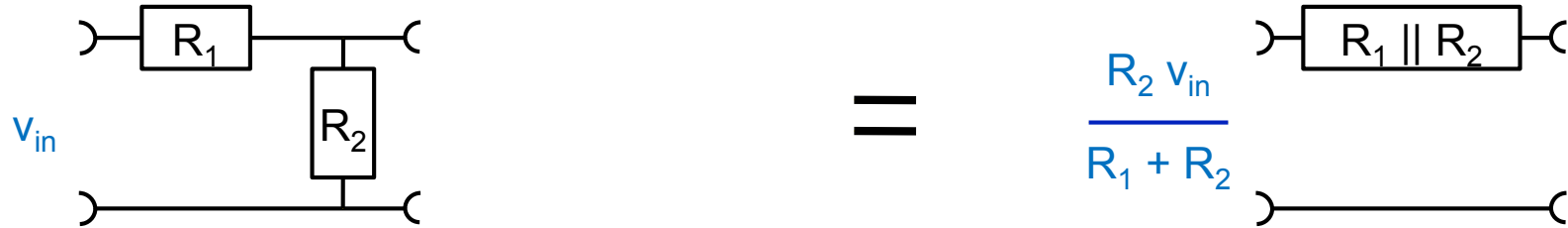
$$\frac{R_2}{R_1 + R_2 + C_2 R_1 R_2 s}$$

`Hdirect == Hdiv`

`True`



# Solution 1.3



$$H_{thenevin} = \frac{g}{1 + s R R C_2} / \cdot \left\{ g \rightarrow \frac{R_2}{R_1 + R_2}, RR \rightarrow \left( \frac{1}{R_1} + \frac{1}{R_2} \right)^{-1} \right\} // \text{Simplify}$$

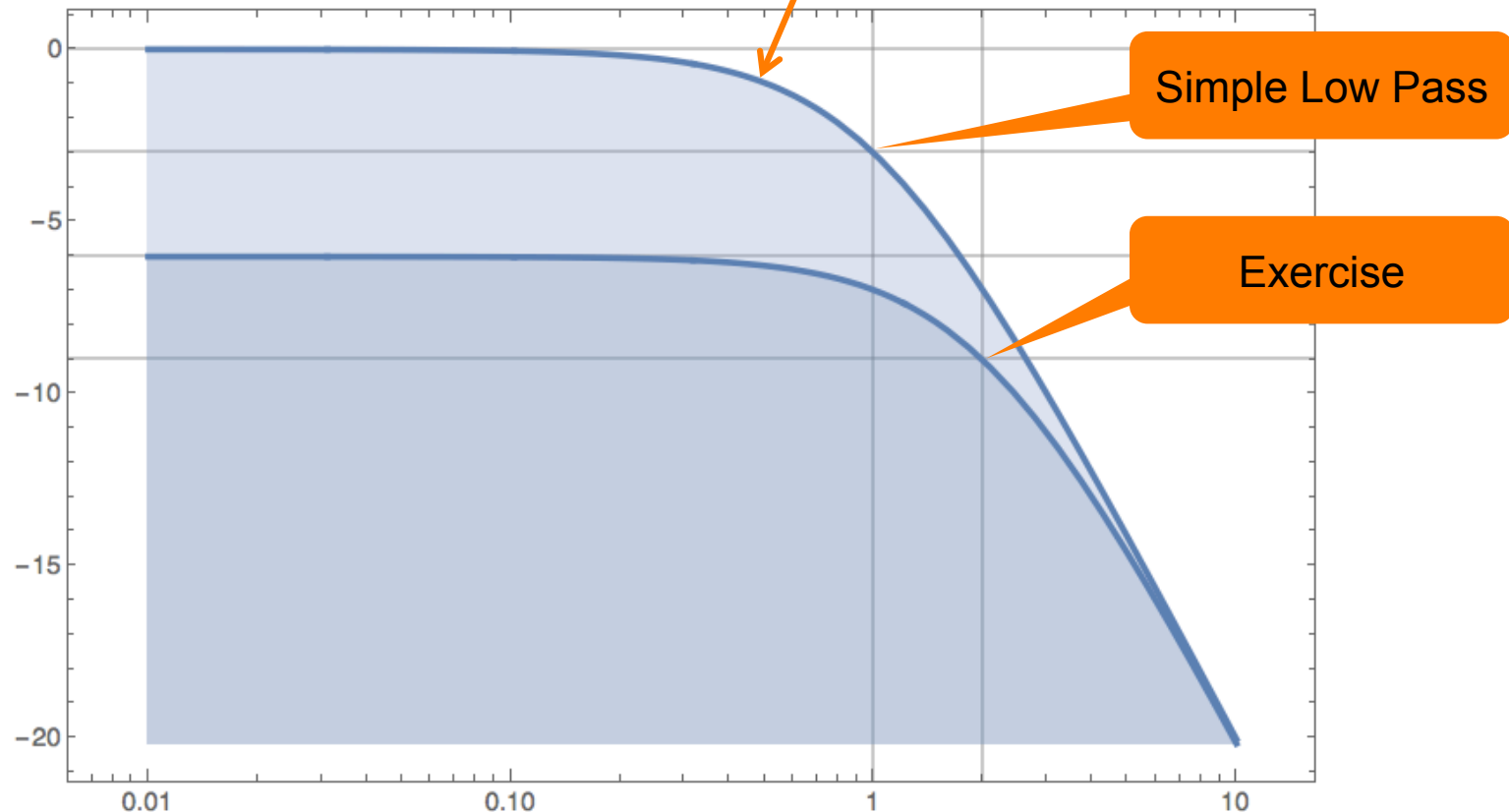
$$\frac{R_2}{R_1 + R_2 + C_2 R_1 R_2 s}$$

Simple Low Pass



# Solution 1.3

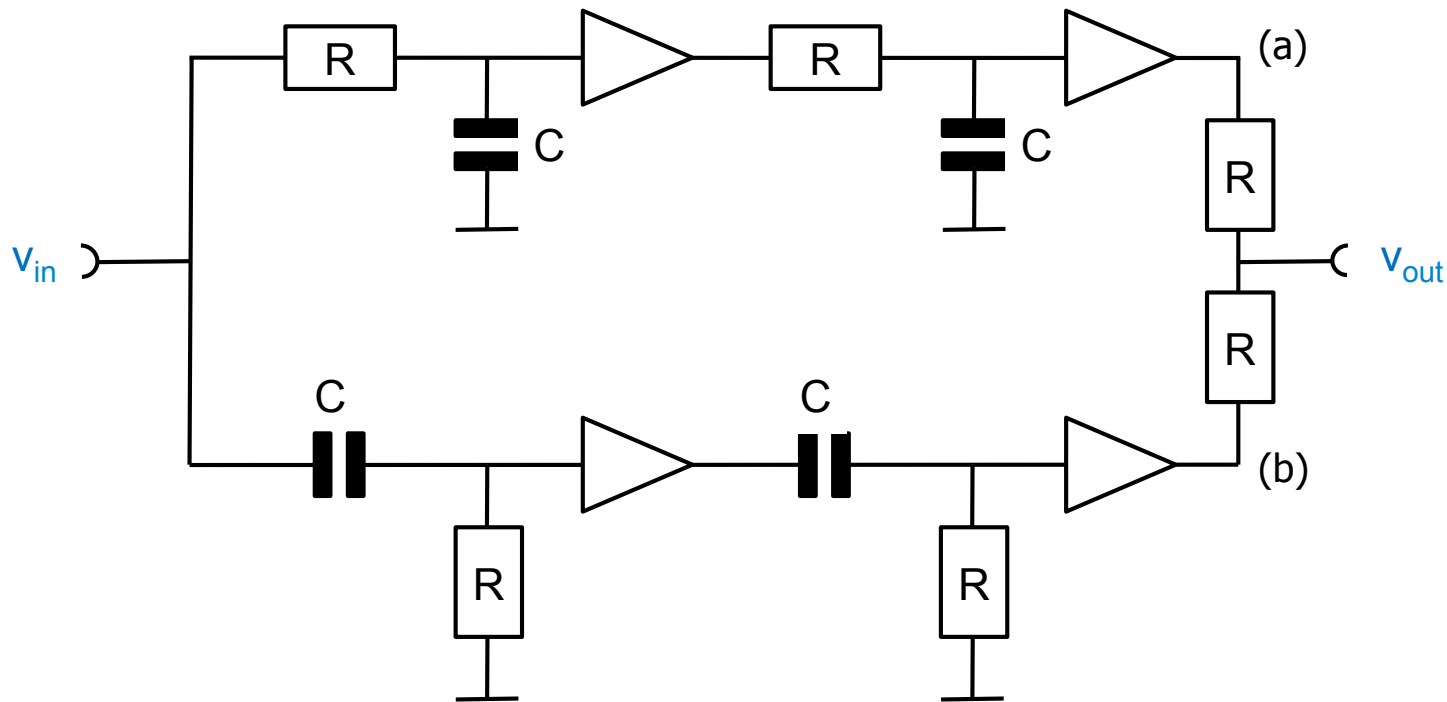
- Compared to the 'simple' Low-Pass:
  - The signal is attenuated by  $R_1/(R_1+R_2)$
  - The time constant is lowered (i.e. the corner frequency is raised)
- Plot for  $R_1 = R_2 = C_2 = 1$ :  $HLP = \sqrt{\frac{1}{1 + i \omega RC} \text{Conjugate} \left[ \frac{1}{1 + i \omega RC} \right]} \cdot \{RC \rightarrow 1\}$





## Exercise 1.4: Notch Filter

- Consider the following circuit made of cascaded High- and Low Pass stages:
  - The resistors at the output just add the signals at (a) and (b)



- What is the output signal at the corner frequency?
  - Explain this by comparing amplitudes *and phases* at (a) and (b)



# Solution 1.4

$$\text{\$Assumptions} = \omega > 0 \ \&\& \ RC > 0; \text{HLP} = \frac{1}{1 + i \omega RC}; \text{HHP} = \frac{i \omega RC}{1 + i \omega RC};$$

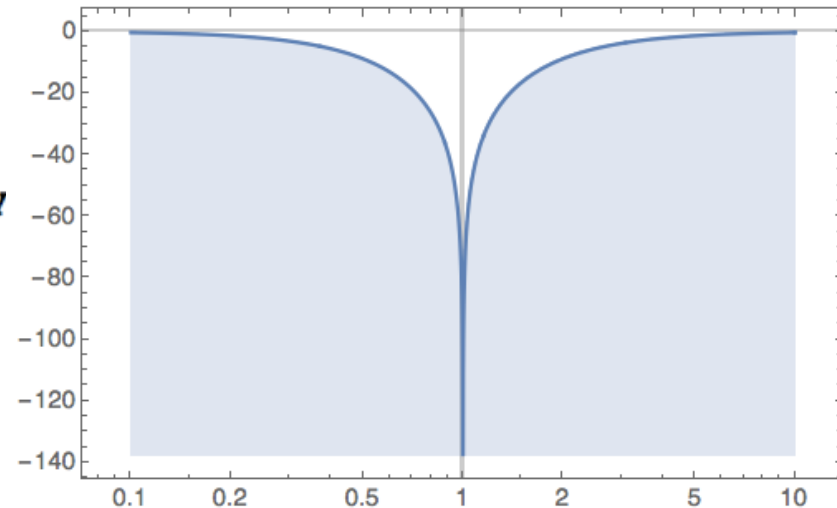
**H = HLP HLP + HHP HHP // Simplify**

$$\frac{-1 + RC^2 \omega^2}{(-i + RC \omega)^2}$$

**HMAG = H Conjugate[H] /. RC -> 1 // FullSimplify**

$$\frac{(-1 + \omega^2)^2}{(1 + \omega^2)^2}$$

**LogLinearPlot[dB[HMAG], {\omega, 0.1, 10}, GridLines -> {{1}, {0}}]**



- At the corner frequency, the signal is fully stopped!
- This is because **the phases** of the two signals are  $\pm 90^\circ$ , i.e. the signals are complementary. For the HP:

$$\text{Limit}\left[\text{ArcTan}\left[\frac{\text{Im}[\text{HLP HLP}]}{\text{Re}[\text{HLP HLP}]}\right] /. \{RC \rightarrow 1\}, \omega \rightarrow 1\right] // \text{ComplexExpand}$$

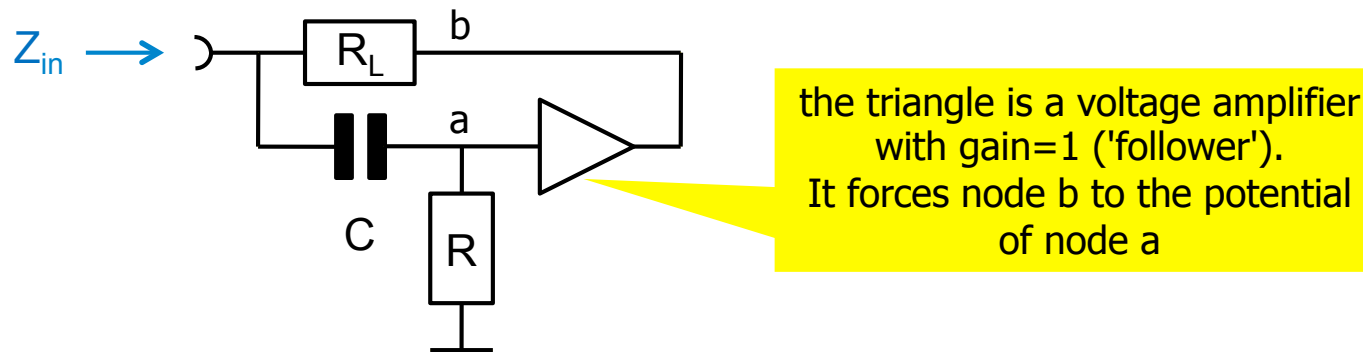
$$\frac{\pi}{2}$$





## Exercise 1.5: Gyrator

- A 'Gyrator' can mimic inductive behaviour, while using only resistors, capacitors and amplifiers
- Consider the following circuit:



- **Calculate** the input impedance  $Z_{in} = U_{in}/I_{in}$  of the circuit
  - (Use Kirchhoff's law at the input node and node a)
- For frequencies  $< 1/C R_L$ , the denominator can be neglected.
- Compare the result to an inductor in series with  $R_L$
- **Simulate.**
  - Note that R should be larger than  $R_L$  (what happens for  $R=R_L$ ?)
  - Plot  $i_{in}$ .
  - Add another capacitor in series to produce a resonant circuit.



# Solution 1.5

## Mathematica:

```
EQin = iin == (vin - va) s C + (vin - vb) / RL /. vb -> va;
```

```
EQa = (vin - va) s C == va / R;
```

```
Eliminate[{EQin, EQa}, va] // Simplify
```

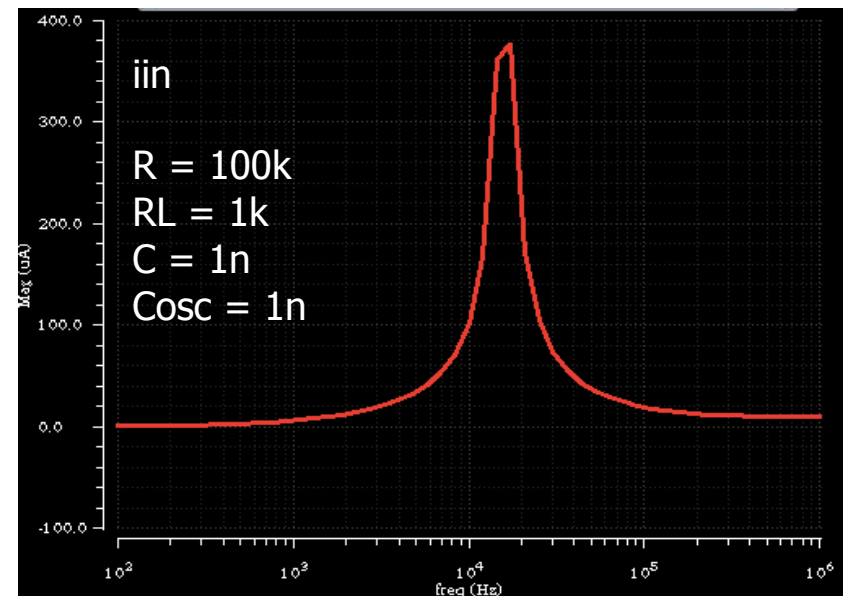
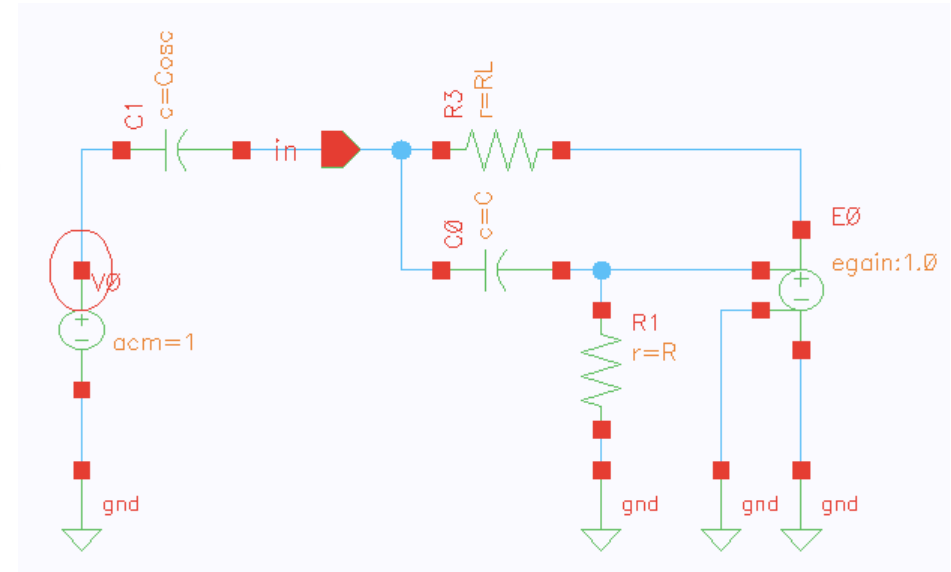
```
iin (RL + C R RL s) == vin + C RL s vin
```

```
sol = Solve[%, iin] // First
```

```
{iin -> (vin + C RL s vin) / (RL (1 + C R s))}
```

```
Zgyrator[s_] = vin / iin /. sol // Simplify
```

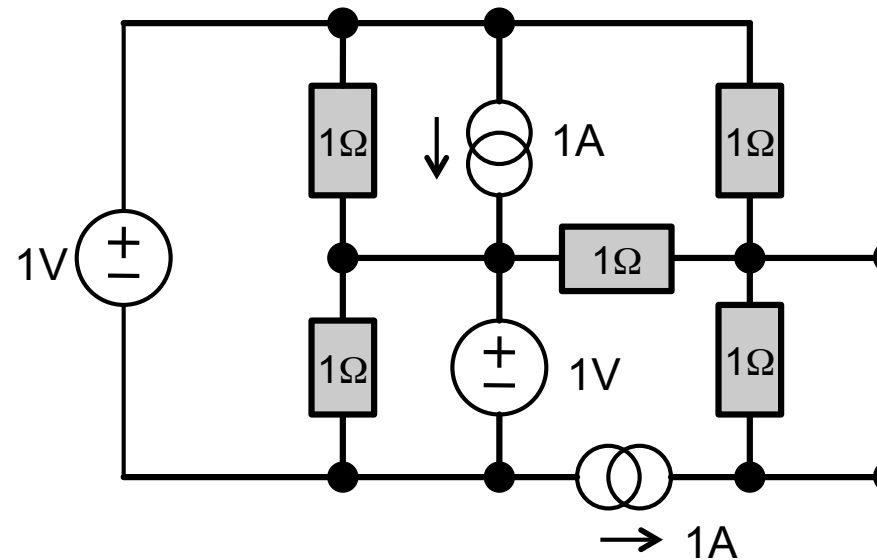
```
(RL + C R RL s) / (1 + C RL s)
```



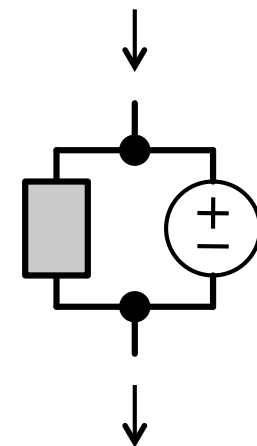


# Exercise 1.2

- Derive the Thévenin Equivalent for the following circuit:



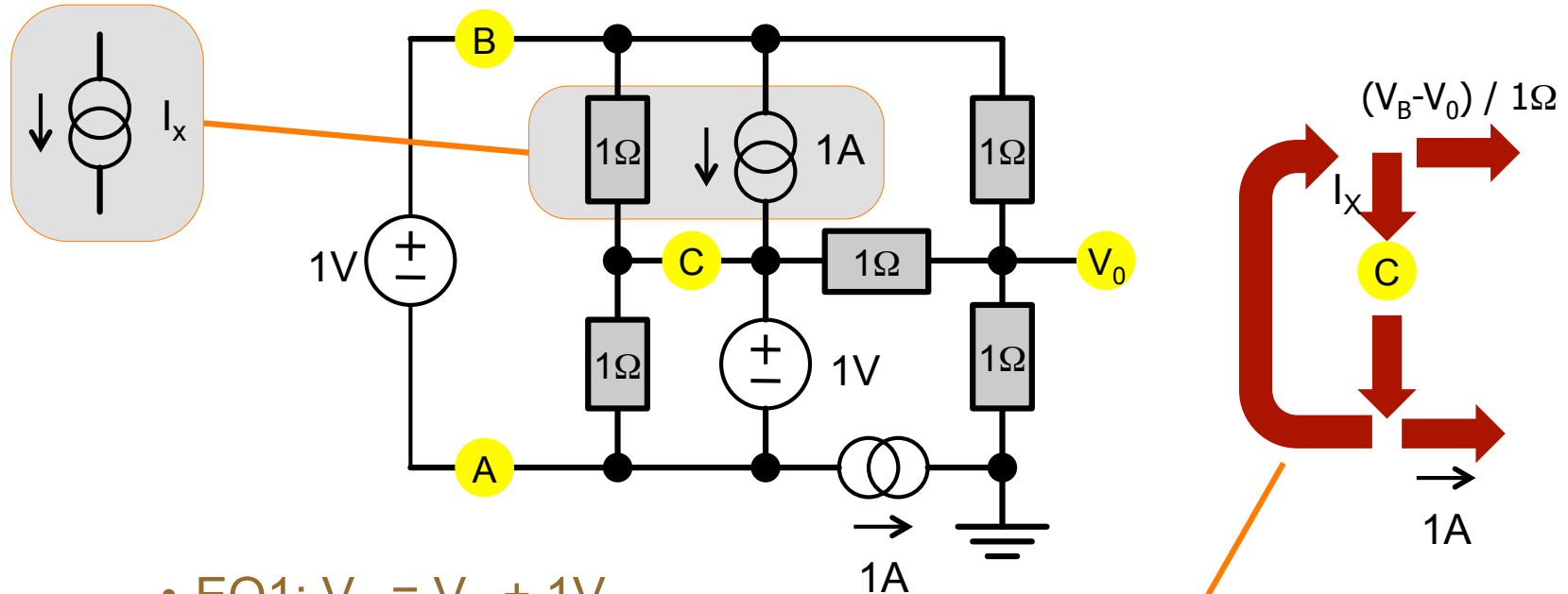
- Hint:**
  - Consider the effect of a fixed voltage source here:
  - The current is 'passed through' (because the voltage on R cannot change!)





# Solution 1.2

- Open circuit voltage (4 nodes  $\rightarrow$  we need 4 equations)



- EQ1:  $V_B = V_A + 1V$
- EQ2:  $V_C = V_A + 1V$
- EQ3:  $(V_B - V_0) / 1\Omega + (V_C - V_0) / 1\Omega = V_0 / 1\Omega$  (current sum at  $V_0$ )
- EQ4:  $I_x = (V_C - V_0) / 1\Omega + 1A + I_x + (V_B - V_0) / 1\Omega$  (current sum at  $V_C$ )

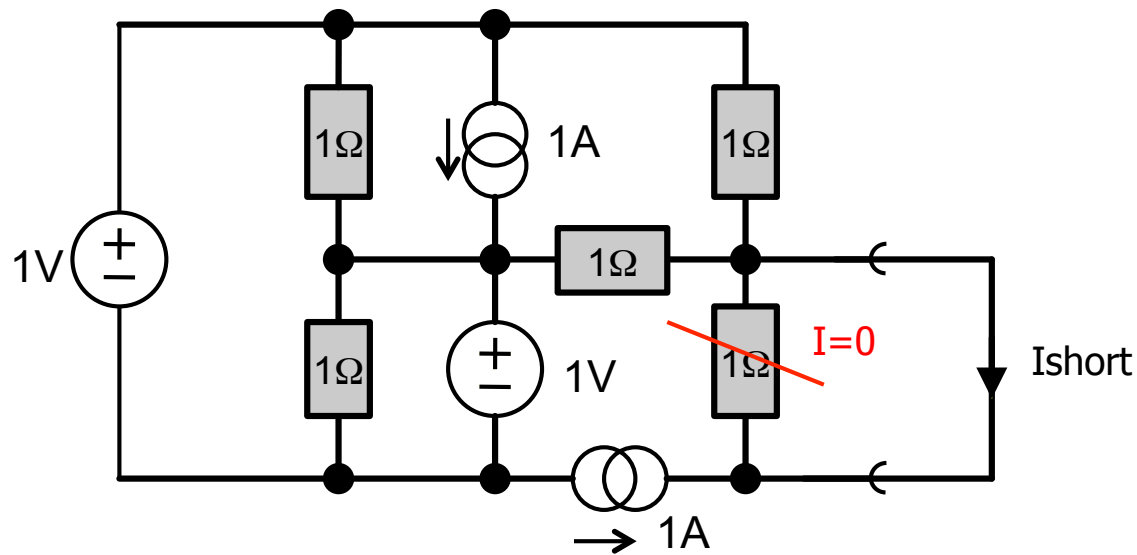
$\rightarrow V_0 = -1V$  ( $V_A = -2.5V, V_B = V_C = -1.5V$ )



# Solution 1.2

▪ Short circuit current:

- This is simple due to the particular circuit: For a short, the voltage at the output – resistor is 0 V → the current in the resistor is 0 A
- $I_{\text{short}} = -1\text{A}$  is fixed by the lower current source



▪ Thévenin Equivalent:

- $V_0 = -1\text{V}$
- $R_V = V_0 / I_{\text{short}} = 1\Omega$

