



# Exercise: Resistors, Capacitors, Thévenin

Prof. Dr. P. Fischer

Lehrstuhl für Schaltungstechnik und Simulation  
Uni Heidelberg



## Exercise 1: Electric Car

- An electric car has a battery of  $E_{\text{Bat}} = 50 \text{ kWh}$  and operates at a voltage of  $V_{\text{Bat}}$ . Assume  $V_{\text{Bat}} = 100 \text{ V}$  for a start.
- It is charged from an ideal voltage source (of  $V_{\text{Bat}}$ ) through a pair of  $L_{\text{Cable}} = 2 \times 3 \text{ m}$  long copper cables of massive cylindrical shape with  $D_{\text{cable}} = 1 \text{ cm}$  diameter.
- The resistivity of copper is  $\rho_{\text{Cu}} = 1.68 \times 10^{-8} \text{ }\Omega\text{m}$ .
  
- How much current would you need to load an empty battery fully in 15 Minutes (if the battery would support that..)?
- How much voltage drop does this current produce in the cable? So what fraction of energy is lost in the cable?
- What power (in Watt) is dissipated in the cable?
  
- How does this change for  $V_{\text{Bat}} = 500 \text{ V}$ ?



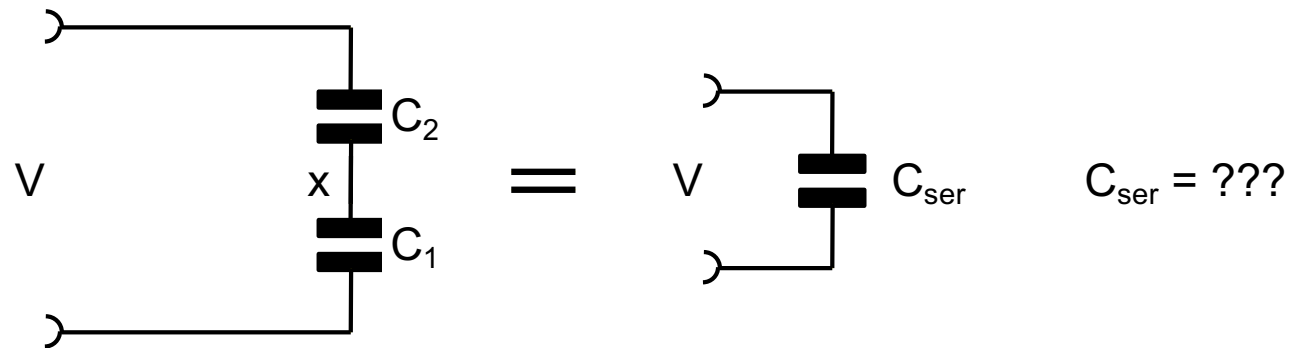
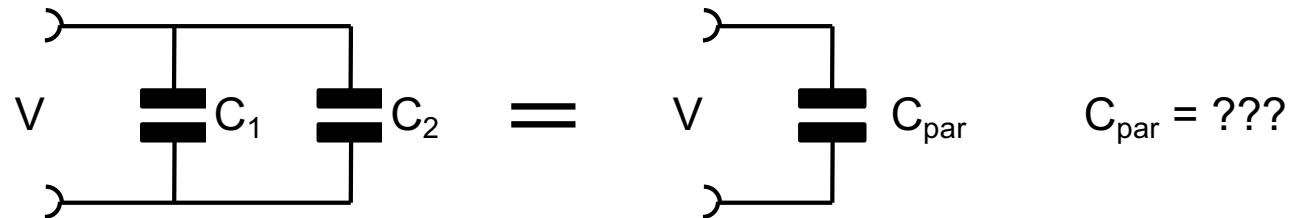
# Solution 1

- Solve  $U_{\text{Bat}} \times I_{\text{Bat}} \times T_{\text{Charge}} = 50000 \text{ VAh}$  for  $I_{\text{Bat}}$  using  $T_{\text{Charge}} = 1/4\text{h}$  and  $V_{\text{Bat}} = 100\text{V}$   
 $\rightarrow I_{\text{Cable}} = 2000 \text{ A}$
- The resistivity of the cable is  $R = \rho L / A$  with  $L = 6\text{m}$  and  $A = \pi (5 \times 10^{-3} \text{ m})^2 = 0.0013 \Omega$ .
- Voltage drop is  $V_{\text{Cable}} = R_{\text{Cable}} \times I_{\text{Cable}} = 2.6\text{V}$  which is 2.6% of  $V_{\text{bat}}$
- Power dissipation in the cable is  $V_{\text{Cable}} \times I_{\text{Cable}} = R_{\text{Cable}} \times I_{\text{Cable}}^2 = 5100 \text{ W (!)}$  – too much
- For  $V_{\text{Bat}} = 500\text{V}$ , this drops quadratically (to 1/25) to 0.1% loss and 200W.



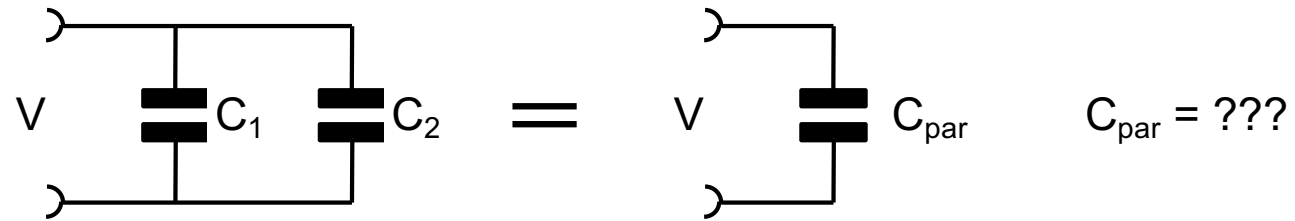
# Exercise 2

- Derive the expressions for the series and parallel connection of capacitors
- Use charge conservation (at node x)





# Solution 2



## 1. Charge conservation:

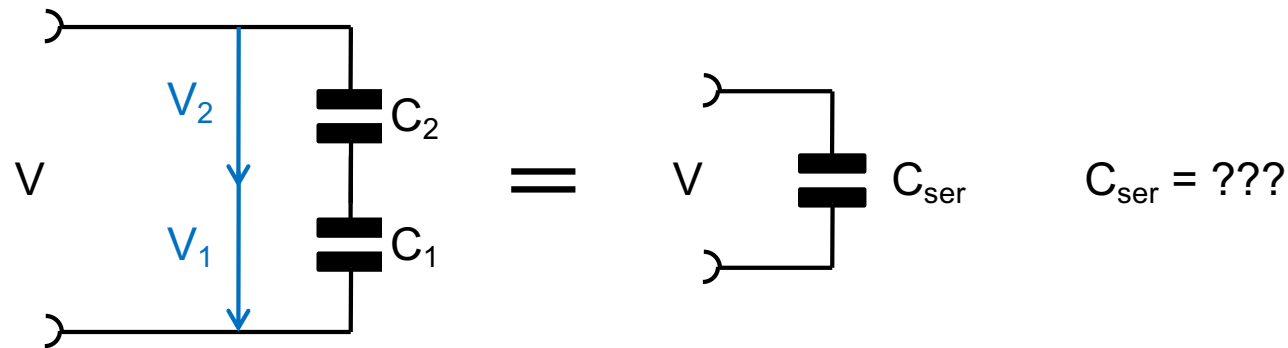
$$V \times C_1 + V \times C_2 = Q_1 + Q_2 = Q_{\text{par}} = V \times C_{\text{par}} \rightarrow C_1 + C_2 = C_{\text{par}}$$

## 2. Kirchhoff & complex impedance:

$$V sC_1 + V sC_2 = i_1 + i_2 = i_{\text{par}} = V sC_{\text{par}} \rightarrow C_1 + C_2 = C_{\text{par}}$$



# Solution 2



## 1. Charge conservation:

Note: no charge can 'escape' the middle node, so that  $Q_1 = Q_2 = Q_{\text{ser}}$

$$V = V_1 + V_2 = Q_1/C_1 + Q_2/C_2 = Q/C_1 + Q/C_2 = Q/C_{\text{ser}}$$

$$\rightarrow 1/C_1 + 1/C_2 = 1/C_{\text{ser}}$$

## 2. Kirchhoff & complex impedance:

$$V_1 sC_1 = V_2 sC_2 \quad \text{and} \quad V_1 + V_2 = V \quad \rightarrow \quad V_1 = V C_2 / (C_1 + C_2)$$

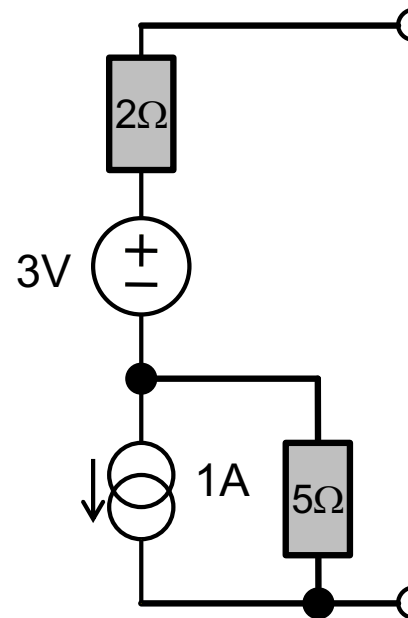
$$\rightarrow i_1 = V_1 sC_1 = V s C_1 C_2 / (C_1 + C_2)$$

$$\rightarrow C_{\text{ser}} = i / (Vs) = i_1 / (Vs) = C_1 C_2 / (C_1 + C_2)$$



## Exercise 3

- Derive the Thévenin Equivalent for the following circuit:



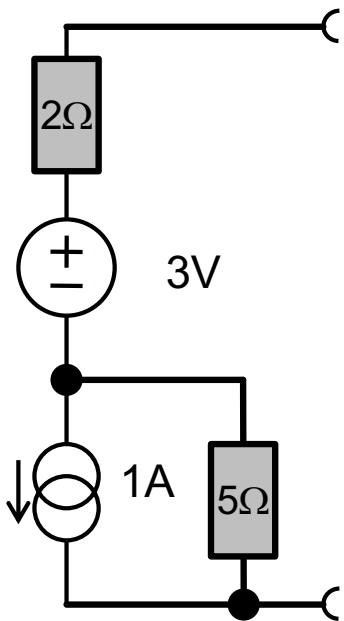
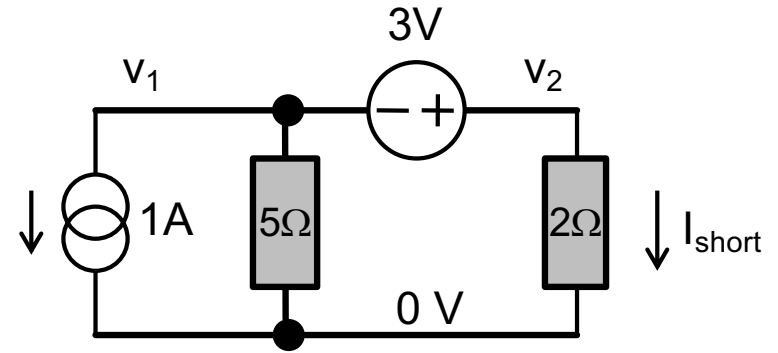
- Try two different methods:
  - Use the Open/Short method with Kirchhoff's rules
  - Convert the I-source part to a voltage source first...



# Solution 3 – Kirchhoff

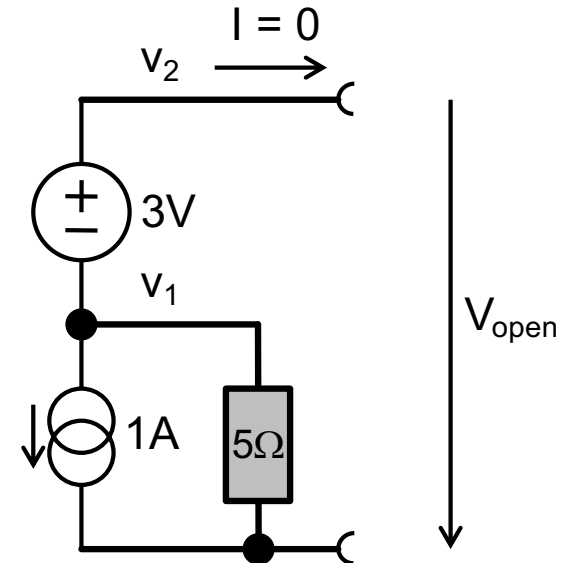
## 1. Short circuit current:

- EQ1:  $1 \text{ A} + v_1 / 5\Omega + v_2 / 2\Omega = 0$
- EQ2:  $v_2 = v_1 + 3\text{V}$
- $\rightarrow v_2 = -4 / 7 \text{ V}$
- $\rightarrow I_{\text{short}} = -2 / 7 \text{ A}$



## 1. Open circuit voltage:

- EQ1:  $1 \text{ A} + v_1 / 5\Omega = 0$
- EQ2:  $v_2 = v_1 + 3\text{V}$
- $\rightarrow v_1 = -5 \text{ V}$
- $\rightarrow v_2 = V_{\text{open}} = -2 \text{ V}$

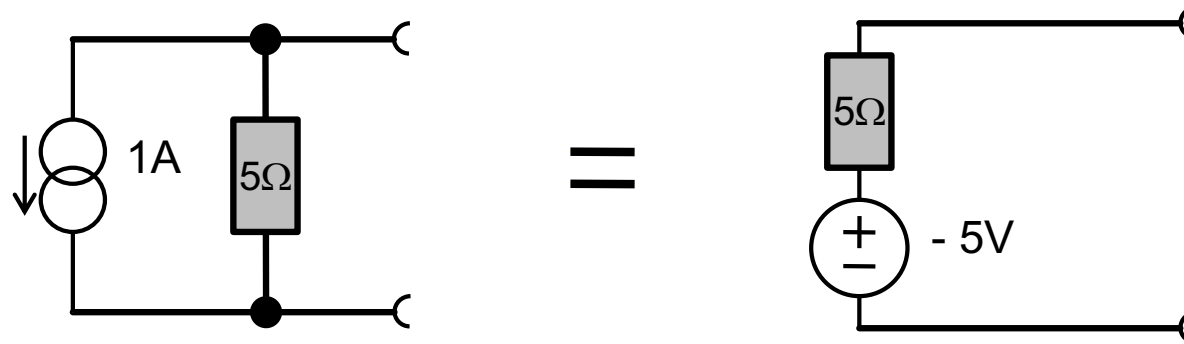


- Source:  $V_0 = V_{\text{open}} = -2 \text{ V}, R_V = V_0 / I_{\text{short}} = 7 \Omega$

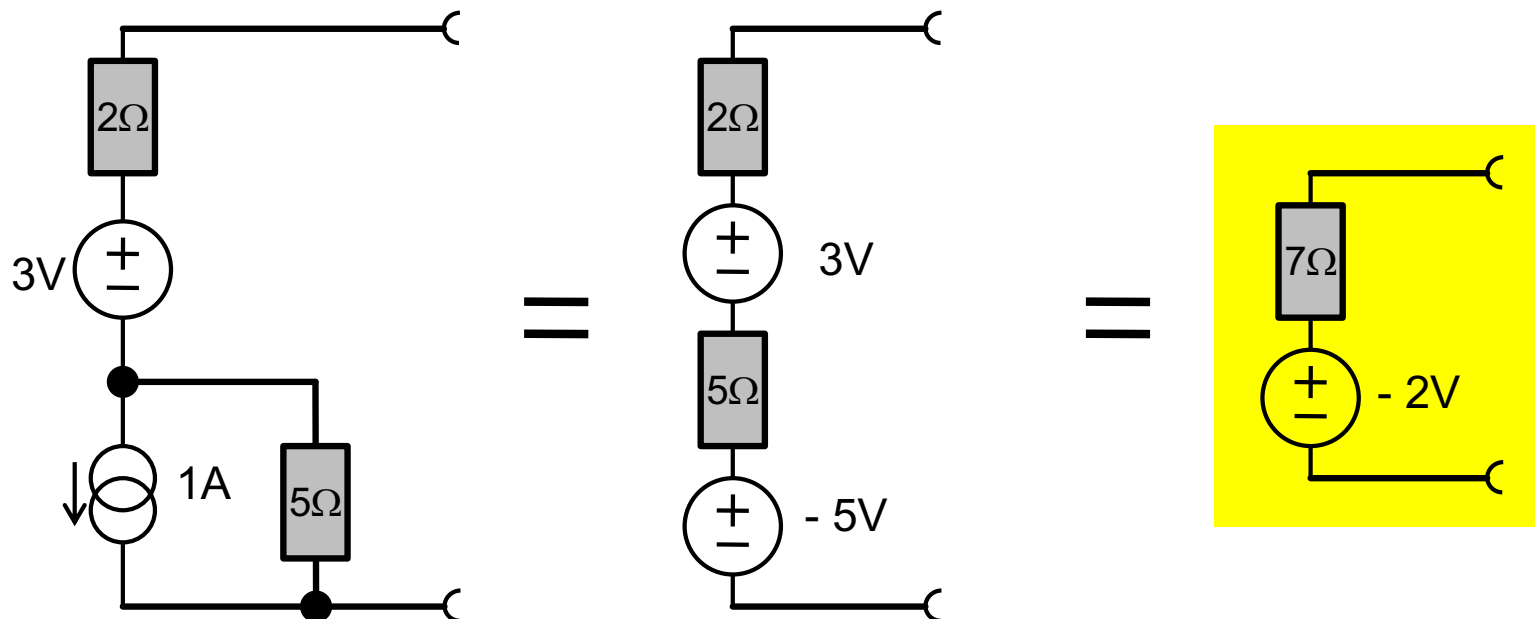


# Solution 3 – Thévenin Transformations

1. Convert the current source to a voltage source:



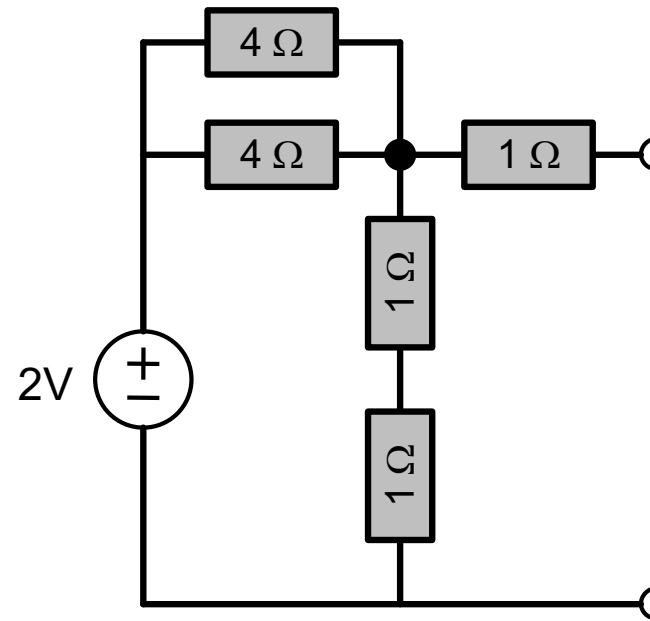
2. Use this in the circuit:





## Exercise 4

- What is the Thévenin Equivalent of the following circuit?

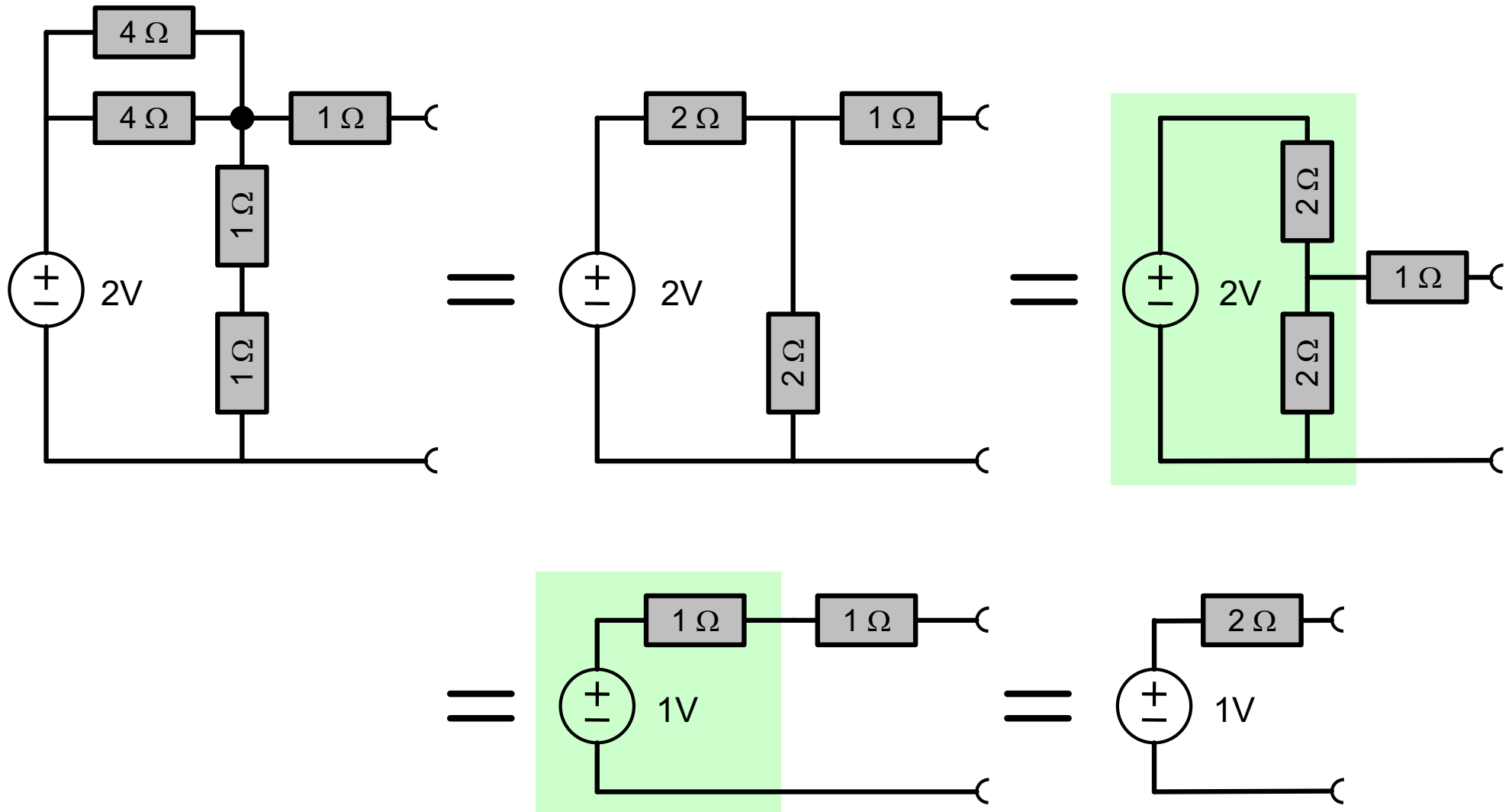


- Use two methods to find the result:
  - parallel / series connection of resistors and your knowledge about the voltage divider
  - short/open method



# Solution 4

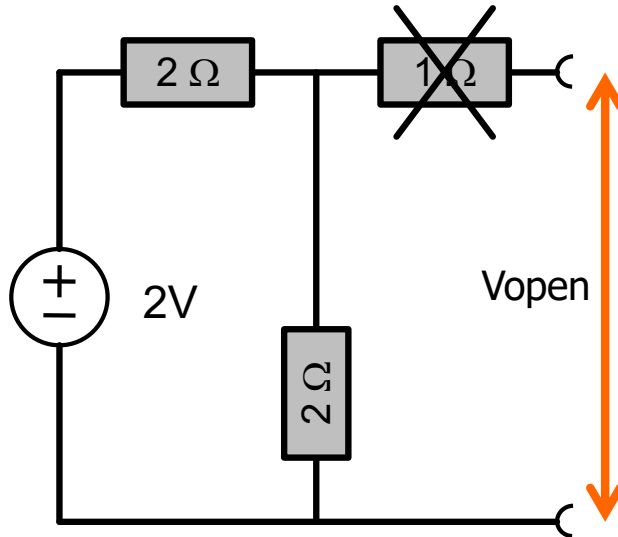
- Parallel-Series Connection, Voltage Divider:



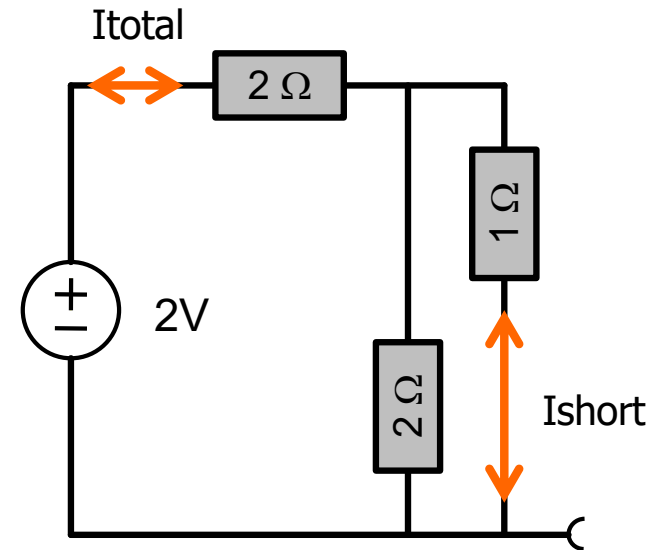


# Solution 5

- Open:  $V_{open} = 1V$



- Short:



$$R_{total} = 2\Omega + 2/3\Omega = 8/3\Omega$$

$$I_{total} = 2V / R_{total} = 3/4 A$$

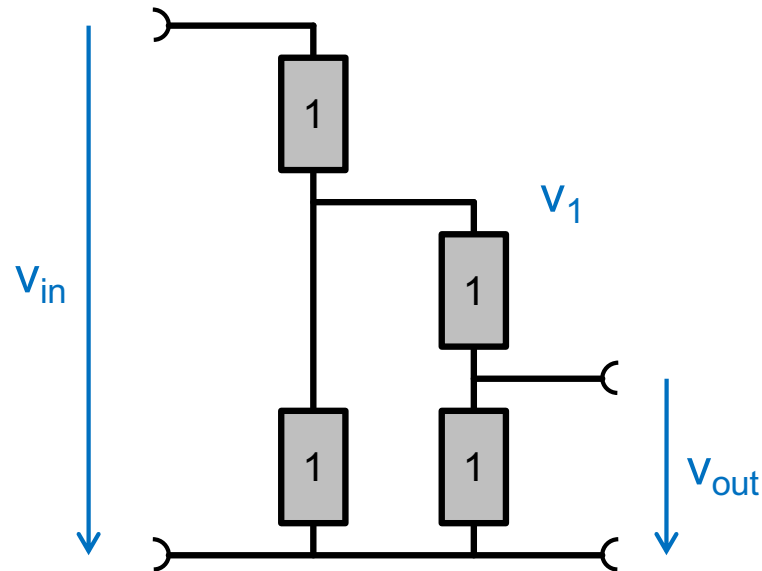
$$I_{short} = 2/3 I_{total} = 1/2 A$$

$$\begin{aligned} Z_{in} &= V_{open} / I_{short} \\ &= 1V / 1/2 A \\ &= 2\Omega \end{aligned}$$



## Exercise 5

- What is the 'gain' (attenuation) of the following voltage divider (all resistors have 1 Ohm):



- Try 3 different methods:
  - Your knowledge of parallel / serial connection of resistors
  - Kirchhoff's law
  - Use your knowledge about the Thévenin equivalent of a voltage divider



# Solution 5

## 1. 'By hand':

- The lower part is a *parallel* connection of  $1\Omega$  and  $2\Omega$ .  
This gives  $(1/1\Omega + 1/2\Omega)^{-1} = 2/3 \Omega$ .
- So we have at node  $v_1$  a voltage divider with  $1\Omega$  and  $2/3 \Omega$ .  
The voltage at  $v_1$  is  $(2/3) / (1+2/3) v_{in} = 2/5 v_{in}$
- The voltage at  $v_{out}$  is half of  $v_1$ , so  $v_{out} = 1/5 v_{in}$

## 2. Kirchhoff

- We have current equations at nodes  $v_1$  and  $v_{out}$ :

$$\text{EQv1} = \frac{v_{in} - v_1}{1} == \frac{v_1}{1} + \frac{v_1 - v_{out}}{1};$$

$$\text{EQvout} = \frac{v_1 - v_{out}}{1} == \frac{v_{out}}{1};$$

`Eliminate[EQv1 && EQvout, v1]`

`5 vout == vin`

`First@Solve[%, vout]`

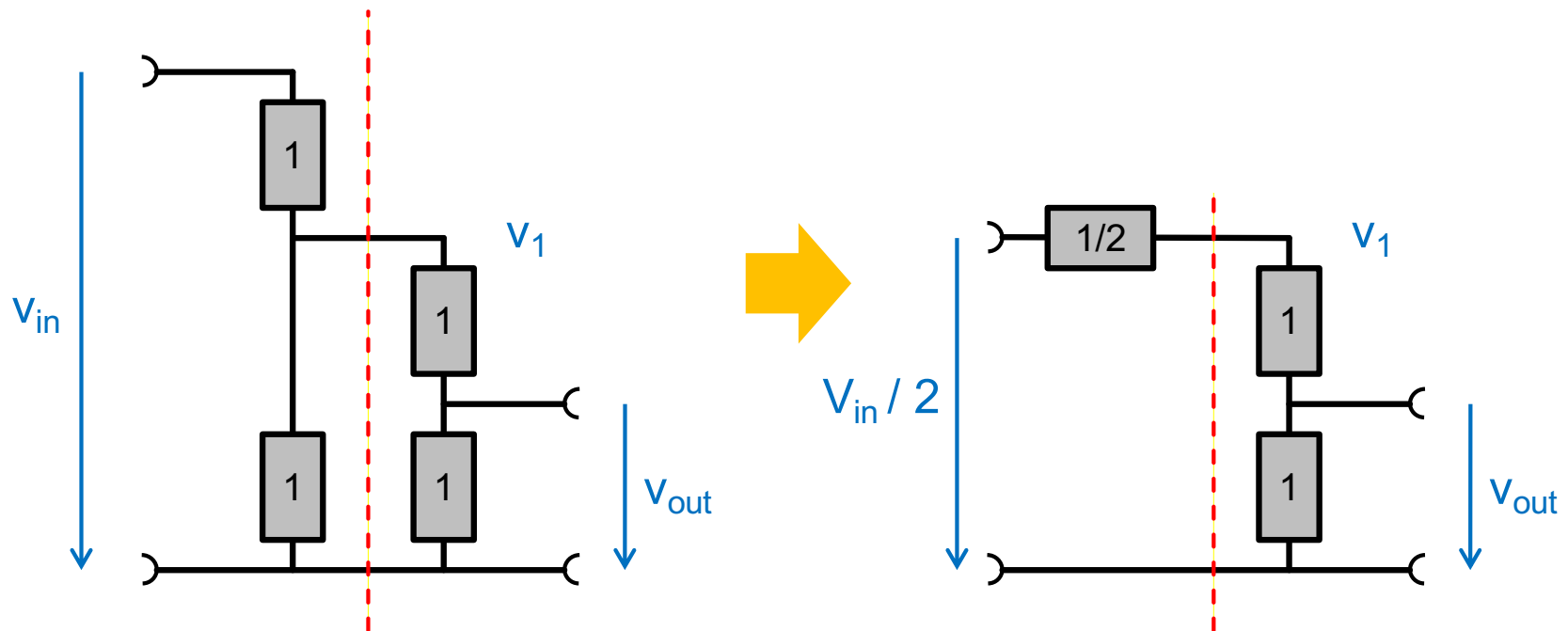
$$\left\{ v_{out} \rightarrow \frac{v_{in}}{5} \right\}$$



# Solution 5

## 3. Thévenin:

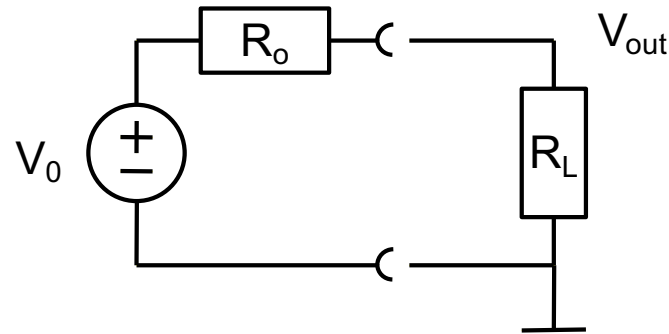
- The first divider (left of the dotted red line) can be replaced by its Thévenin equivalent of a voltage source with  $v_{in}/2$  and an outputs resistance of  $1/2 \Omega$ .
- This creates a divider of  $1/2.5$  of a voltage  $v_{in}/2$ , so that we get  $v_{in}/5$ .





## Exercise 6

- A voltage source with voltage  $V_0$  and output resistance  $R_0$  is loaded by a resistor  $R_L$ :



- What is the output voltage  $V_{out}$  ?
- Which current flows in  $R_L$  ?
- What power ( $P = U I$ ) is dissipated in  $R_L$  ?
  - Check that nothing is dissipated for  $R_L=0$  and  $R_L \rightarrow \infty$
- For which value of  $R_L$  is the dissipation maximized?
  - What is the dissipation?



# Solution 6

In[29]:= **Vout** =  $V_0 \frac{R_L}{R_0 + R_L}$  ;

In[30]:= **Iout** =  $\frac{V_{out}}{R_L}$

Out[30]=  $\frac{V_0}{R_0 + R_L}$

In[31]:= **Pout** = **Vout** **Iout**

Out[31]=  $\frac{R_L V_0^2}{(R_0 + R_L)^2}$

In[38]:= **Table**[**Limit**[**Pout**, **RL** → **x**], {**x**, {**0**, **∞**}}

Out[38]= {**0**, **0**}

In[39]:= **Solve**[**D**[**Pout**, **RL**] == **0**, **RL**] // **First**

Out[39]= {**RL** → **R0**}

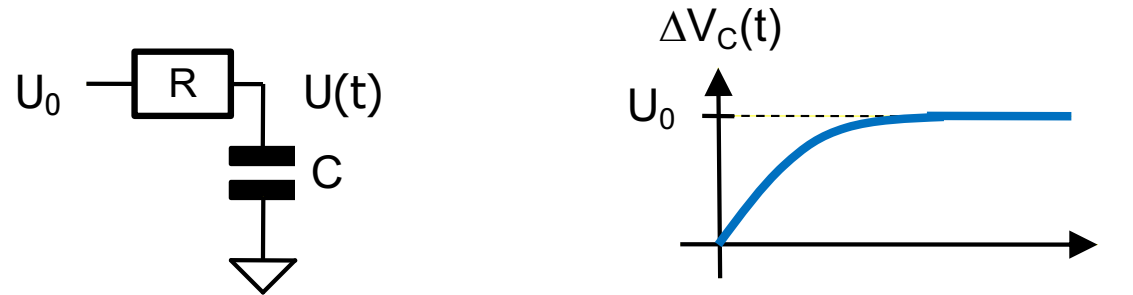
In[40]:= **Pout** /. %

Out[40]=  $\frac{V_0^2}{4 R_0}$



# Exercise 7

- We consider charging of a capacitor  $C$  through a resistor  $R$  to a voltage  $U_0$ .



- Show that  $U(t) = U_0 - U_0 e^{-\frac{t}{RC}}$  satisfies the differential equation
- Simplify  $U(t)$  for small times  $t \ll RC$ .
- What is the initial slope ?
- Derive this slope directly (assuming  $U(0) = 0$ ).



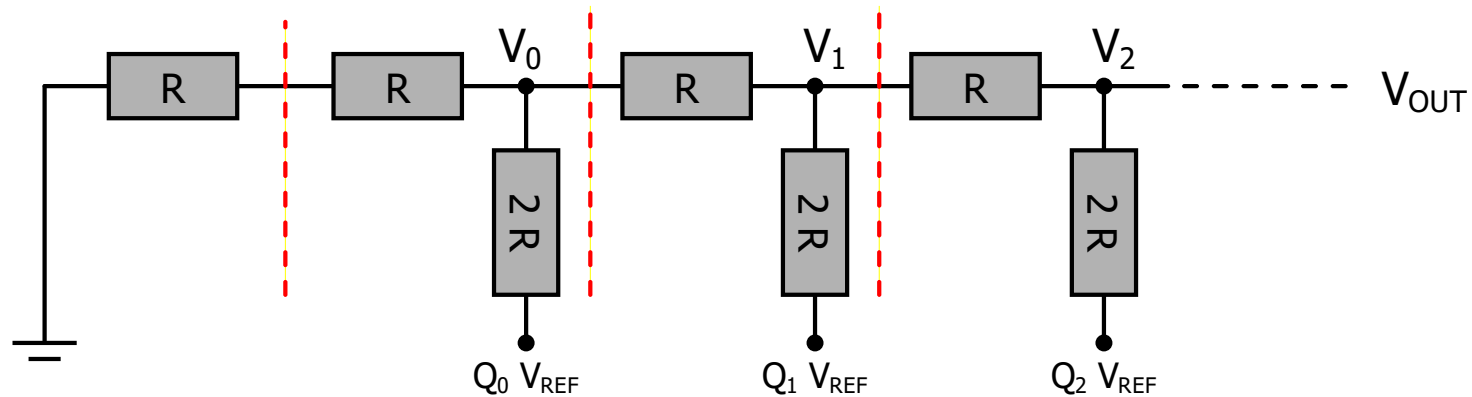
## Solution 7

- For a capacitor, we have  $dU/dt = I/C$ .  
And we have  $I = (U_0 - U)/R$   
So the DGL is  $U'(t) = (U_0 - U)/RC$
- The proposed solution  $U(t) = U_0 - U_0 \text{Exp}(-t/RC)$  gives  
The left hand side:  $U'(t) = U_0/RC \text{Exp}(-t/RC)$   
And the right side:  $U_0 \text{Exp}(-t/RC) / RC$ , i.e. the same.
- For small  $x$ , the  $\text{Exp}(x)$  is  $\sim 1+x$ , therefore  
 $U(t) \sim U_0 - U_0 (1-t/RC) = t U_0/RC$
- The slope is derivative of this, i.e  $U_0/RC$
- This is a charging  $I/C$  with an initial current  $U_0/R$



## Exercise 8: R-2R DAC

- Digital-Analog-Converters (DACs) convert a digital (normally binary coded) value into a voltage (or current) which is normally proportional to the digital value.
- A simple circuit is the R-2R DAC:

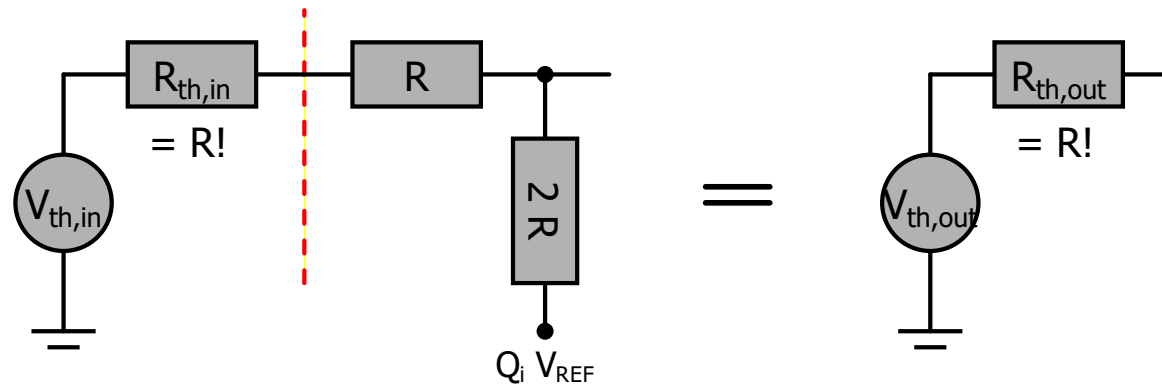


- The input voltages at the lower side of the  $2R$  resistors is either  $0V$  or some  $V_{REF}$ , depending of the binary bit ( $Q_0$  is the Least Significant Bit, LSB).
- Show that the output voltage of a R-2R DAC with an arbitrary number  $N$  of bits is proportional to  $Q$ !
  - Hint: Replace the circuit by Thévenin equivalents at the red lines from left to right.



## Solution 8

- Assume the left (driving) side of a 'red line' is given by  $V_{th,in}$  and  $R_{th,in}$ :



- The output resistance is  $R_{th,out} = (R_{th,in} + R) \parallel 2R$ .  
For  $R_{th,in} = R$ , we get again  $R_{th,out} = 2R \parallel 2R = R!$   
Therefore  $R_{th} = R$  in all stages.
- Therefore,  $R_{th,in}$  and the serial  $R$  add up to  $2R$  and we *always* have a 1:1 divider!:  $V_{th,out}$  is the average of  $V_{th,in}$  and  $Q V_{REF}$
- With  $V_{th,in}$  of the 0-th stage being 0, we get  
 $V_0 = \text{Average}(0, Q_0 V_{REF}) = Q_0 / 2 \times V_{REF}$   
 $V_1 = \text{Average}(Q_0 / 2 \times V_{REF} / 2, Q_1 V_{REF}) = (Q_1 / 2 + Q_0 / 4) V_{REF}$   
 $V_2 = \dots = (Q_2 / 2 + Q_1 / 4 + Q_0 / 8) V_{REF}$   
and so on. This is the desired linear relationship.